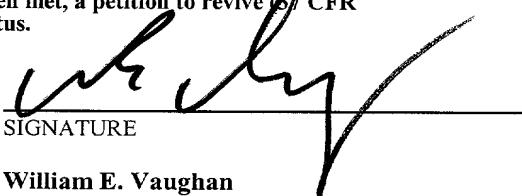


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| U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE (REV. 11-2000) | | ATTORNEY'S DOCKET NUMBER 112740-532 | |
| TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371 | | U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR 10/088130 | |
| INTERNATIONAL APPLICATION NO. PCT/DE00/03105 | INTERNATIONAL FILING DATE 07 September 2000 | PRIORITY DATE CLAIMED 13 September 1999 | |
| TITLE OF INVENTION SYSTEM FOR SYNCHRONIZING COMMUNICATIONS SYSTEM COMPONENTS COUPLED VIA A COMMUNICATIONS NETWORK | | | |
| APPLICANT(S) FOR DO/EO/US Juergen Heitmann | | | |
| Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information: | | | |
| <ol style="list-style-type: none"> 1. <input checked="" type="checkbox"/> This is a FIRST submission of items concerning a filing under 35 U.S.C. 371. 2. <input type="checkbox"/> This is a SECOND or SUBSEQUENT submission of items concerning a filing under 35 U.S.C. 371. 3. <input checked="" type="checkbox"/> This is an express request to begin national examination procedures (35 U.S.C. 371(f)). The submission must include items (5), (6), (9) and (24) indicated below. <input checked="" type="checkbox"/> The US has been elected by the expiration of 19 months from the priority date (Article 31). <input checked="" type="checkbox"/> A copy of the International Application as filed (35 U.S.C. 371 (c) (2)) <ol style="list-style-type: none"> a. <input checked="" type="checkbox"/> is attached hereto (required only if not communicated by the International Bureau). b. <input type="checkbox"/> has been communicated by the International Bureau. c. <input type="checkbox"/> is not required, as the application was filed in the United States Receiving Office (RO/US). <input checked="" type="checkbox"/> An English language translation of the International Application as filed (35 U.S.C. 371(c)(2)). <ol style="list-style-type: none"> a. <input checked="" type="checkbox"/> is attached hereto. b. <input type="checkbox"/> has been previously submitted under 35 U.S.C. 154(d)(4). <input checked="" type="checkbox"/> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371 (c)(3)) <ol style="list-style-type: none"> a. <input type="checkbox"/> are attached hereto (required only if not communicated by the International Bureau). b. <input checked="" type="checkbox"/> have been communicated by the International Bureau. c. <input type="checkbox"/> have not been made; however, the time limit for making such amendments has NOT expired. d. <input type="checkbox"/> have not been made and will not be made. <input checked="" type="checkbox"/> An English language translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)). <input checked="" type="checkbox"/> An oath or declaration of the inventor(s) (35 U.S.C. 371 (c)(4)). 10. <input type="checkbox"/> An English language translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371 (c)(5)). 11. <input checked="" type="checkbox"/> A copy of the International Preliminary Examination Report (PCT/IPEA/409). 12. <input checked="" type="checkbox"/> A copy of the International Search Report (PCT/ISA/210). | | | |
| Items 13 to 20 below concern document(s) or information included: | | | |
| <ol style="list-style-type: none"> 13. <input checked="" type="checkbox"/> An Information Disclosure Statement under 37 CFR 1.97 and 1.98. 14. <input checked="" type="checkbox"/> An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included. 15. <input checked="" type="checkbox"/> A FIRST preliminary amendment. 16. <input type="checkbox"/> A SECOND or SUBSEQUENT preliminary amendment. 17. <input checked="" type="checkbox"/> A substitute specification. 18. <input type="checkbox"/> A change of power of attorney and/or address letter. 19. <input type="checkbox"/> A computer-readable form of the sequence listing in accordance with PCT Rule 13ter.2 and 35 U.S.C. 1.821 - 1.825. 20. <input type="checkbox"/> A second copy of the published international application under 35 U.S.C. 154(d)(4). 21. <input type="checkbox"/> A second copy of the English language translation of the international application under 35 U.S.C. 154(d)(4). 22. <input checked="" type="checkbox"/> Certificate of Mailing by Express Mail 23. <input type="checkbox"/> Other items or information: | | | |

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|----------------------------------------------------------------|--------------------------------------------------------|-----------------------------------------------|
| U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR 10/088130 | INTERNATIONAL APPLICATION NO. PCT/DE00/03105 | ATTORNEY'S DOCKET NUMBER 112740-532 |
|----------------------------------------------------------------|--------------------------------------------------------|-----------------------------------------------|

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| 24. The following fees are submitted: | CALCULATIONS PTO USE ONLY | |
| BASIC NATIONAL FEE (37 CFR 1.492 (a) (1) - (5)) : | | |
| <input type="checkbox"/> Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO and International Search Report not prepared by the EPO or JPO \$1040.00 <input checked="" type="checkbox"/> International preliminary examination fee (37 CFR 1.482) not paid to USPTO but International Search Report prepared by the EPO or JPO \$890.00 <input type="checkbox"/> International preliminary examination fee (37 CFR 1.482) not paid to USPTO but international search fee (37 CFR 1.445(a)(2)) paid to USPTO \$740.00 <input type="checkbox"/> International preliminary examination fee (37 CFR 1.482) paid to USPTO but all claims did not satisfy provisions of PCT Article 33(1)-(4) \$710.00 <input type="checkbox"/> International preliminary examination fee (37 CFR 1.482) paid to USPTO and all claims satisfied provisions of PCT Article 33(1)-(4) \$100.00 | | |
| ENTER APPROPRIATE BASIC FEE AMOUNT = \$890.00 | | |
| Surcharge of \$130.00 for furnishing the oath or declaration later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492 (e)). \$0.00 | | |
| CLAIMS | NUMBER FILED | NUMBER EXTRA |
| Total claims | 14 - 20 = | 0 |
| Independent claims | 2 - 3 = | 0 |
| Multiple Dependent Claims (check if applicable). | | <input type="checkbox"/> \$0.00 |
| TOTAL OF ABOVE CALCULATIONS = | | \$890.00 |
| <input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27). The fees indicated above are reduced by 1/2. \$0.00 | | |
| SUBTOTAL = | | \$890.00 |
| Processing fee of \$130.00 for furnishing the English translation later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492 (f)). \$0.00 | | |
| TOTAL NATIONAL FEE = | | \$890.00 |
| Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31) (check if applicable). | | <input type="checkbox"/> \$0.00 |
| TOTAL FEES ENCLOSED = | | \$890.00 |
| | | Amount to be: refunded \$ |
| | | charged \$ |
| a. <input checked="" type="checkbox"/> A check in the amount of \$890.00 to cover the above fees is enclosed. b. <input type="checkbox"/> Please charge my Deposit Account No. _____ in the amount of _____ to cover the above fees. A duplicate copy of this sheet is enclosed. c. <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 02-1818 A duplicate copy of this sheet is enclosed. d. <input type="checkbox"/> Fees are to be charged to a credit card. WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038. | | |
| NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status. | | |
| SEND ALL CORRESPONDENCE TO: | | |
| William E. Vaughan (Reg. No. 39,056) Bell, Boyd & Lloyd LLC P.O. Box 1135 Chicago, Illinois 60690 312-807-4292 | | |
|  SIGNATURE William E. Vaughan NAME 39,056 REGISTRATION NUMBER March 13, 2002 DATE | | |

CERTIFICATE OF MAILING BY "EXPRESS MAIL" (37 CFR 1.10)

Applicant(s): Juergen Heitmann

Docket No.

112740-532

Serial No.

Filing Date

Examiner

Group Art Unit

Invention: SYSTEM FOR SYNCHRONIZING COMMUNICATIONS SYSTEM COMPONENTS COUPLED VIA A COMMUNICATIONS NETWORK

I hereby certify that the following correspondence:

Transmittal letter to the United States Designated/Elected Office in duplicate, International application as originally filed, amended claims, English translation, amended claims, Preliminary Amendment, Prel. Examination Report, IDS, PTO 1449, references, search report, executed declaration and power of attorney, filing fee \$890.00, postcard (see enclosed envelope for executed assignment and fee)

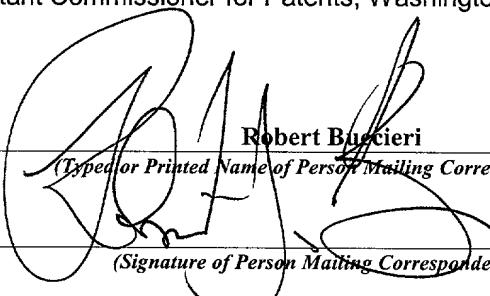
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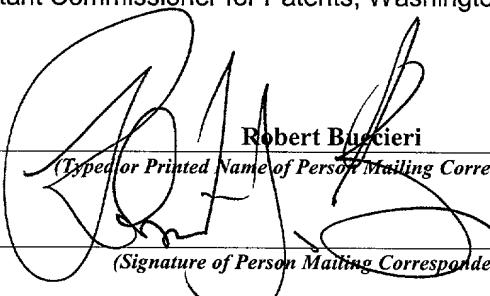
37 CFR 1.10 in an envelope addressed to: The Assistant Commissioner for Patents, Washington, D.C. 20231 on

March 13, 2002

(Date)


Robert Buccieri

(Typed or Printed Name of Person Mailing Correspondence)


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OF THE UNITED STATES PATENT AND TRADEMARK OFFICE
UNDER THE PATENT COOPERATION TREATY-CHAPTER II

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PRELIMINARY AMENDMENT

APPLICANT: Juergen Heitmann DOCKET NO.: 112740-532
SERIAL NO: GROUP ART UNIT:
FILED: EXAMINER:
INTERNATIONAL APPLICATION NO.: PCT/DE00/03105
INTERNATIONAL FILING DATE 07 September 2000
INVENTION: SYSTEM FOR SYNCHRONIZING COMMUNICATIONS
SYSTEM COMPONENTS COUPLED VIA A
COMMUNICATIONS NETWORK

Assistant Commissioner for Patents,
Washington, D.C. 20231

10

Sir:

Please amend the above-identified International Application before entry into the National stage before the U.S. Patent and Trademark Office under 35 U.S.C. §371 as follows:

15

In the Specification:

Please replace the Specification of the present application, including the Abstract, with the following Substitute Specification:

SPECIFICATION

TITLE OF THE INVENTION

SYSTEM FOR SYNCHRONIZING COMMUNICATIONS SYSTEM

5 COMPONENTS COUPLED VIA A COMMUNICATIONS NETWORK

BACKGROUND OF THE INVENTION

In the course of current development taking place, communications systems and control thereof are becoming increasingly decentralized. In this regard, a 10 communications system is split into individual subsystems coupled via a communications network, such as a local area network (LAN) or a network based on an internet protocol (IP). This allows, by way of example, components of a relatively large exchange to be distributed over a communications network.

Contemporary communications systems normally provide a large number of 15 communications services and service features. For some of these communications services and service features, such as for "CBO services" (continuous bit stream operation), which include fax, modem, voice and video transmissions, it is necessary for the respective communications system components involved to be in sync for communications data which are to be transmitted.

Arrangements for synchronizing communications system components coupled 20 via a communications network are known in communications networks using direct SDH (synchronous digital hierarchy) or PDH (plesiochronous digital hierarchy) based transmission; e.g.; from section 8 of ITU-T recommendation G.803 and the references indicated therein. In this case, a reference clock is transmitted to the communications system components which are to be synchronized on the physical layer of the 25 transmission protocol used. However, transmitting a reference clock in the physical layer requires continuous layer 1 connections to the individual communications system components. As such, relatively complex communications network structures can be produced only with a great deal of effort, however. In communications networks which can be configured more flexibly, such as local area networks (LAN) or internet 30 protocol based networks, continuous layer 1 connections generally are not provided.

It is an object of the present invention to specify a system which is more flexible than the prior art in order to synchronize communications system components

coupled via a communications network for communications data which are to be transmitted.

SUMMARY OF THE INVENTION

Accordingly, pursuant to the present invention, to synchronize communications system components coupled via a communications network, they are sent time information from a time information transmitter. The communications system components' orienting of a respectively dedicated time dimension to respective time information received from the time information transmitter synchronizes these communications system components with one another.

A communications system component is synchronized by readjusting the clock frequency of a clock generator intended to prescribe the transmission data rate for communications data whose transmission involves the communications system component in question. In this case, the clock frequency is readjusted by comparing received time information with a current time value on a real time clock whose timing is controlled, in accordance with the present invention, by a timing pulse from the clock generator actually provided for prescribing the transmission data rate for communications data which are to be transmitted. The readjustment of the clock generator's clock frequency is thus indirectly used to adjust the real time clock itself on the basis of the time information received. This indirect adjustment prevents abrupt changes in the time indicated by the real time clock and attenuates effects of delay time fluctuations in received time information.

The inventive system is largely independent of the type of communications network coupling the communications system components. Thus, by way of example, the communications network used can be a "local area network" (LAN) or an internet protocol based communications network.

In accordance with one advantageous embodiment of the present invention, the clock generator in a communications system component can be temperature stabilized or temperature compensated. To increase the clock accuracy, the clock generator also can be produced on the basis of the "2-oscillator concept". In this context, a main oscillator and a temperature stabilized or temperature compensated reference oscillator which regulates the clock frequency of the main oscillator and otherwise freewheels is provided. The more accurate the clock generator in a communications system

component, the longer the time intervals for which the communications system component in question remains in sync even without receiving time information.

In accordance with another advantageous embodiment of the present invention, a communications system component can be sent the time information wirelessly; e.g., from a GPS satellite (global positioning system), from a time signal transmitter, such as DCF77, or from a time information transmitter associated with the communications system. For this purpose, the time information reception device in the communications system component in question has a radio reception device for wirelessly receiving the time information. Due to the very short delay time for time information transmitted by radio, this allows very accurate synchronization to be achieved.

Alternatively, a communications system component also can be sent time information via the communications network from a time information transmitter, such as in the form of a time information server, which is likewise coupled to the communications network. As such, an existing network infrastructure also can be used for synchronizing the communications system components. This alternative allows a complex radio reception device to be dispensed with in the communications system components which are to be synchronized. Instead, time information easily can be received by coupling the time information reception device in a communications system component to the communications network via a network interface and providing it with the ability to extract time information from a data stream transmitted via the communications network.

In accordance with one advantageous embodiment of the present invention, a communications system component can have a time request device so that it can request time information from the time information transmitter via the communications network. In this case, the request can be made preferably using known network protocols, such as the “network time protocol” (NTP) or the “digital time synchronization protocol” (DTSS).

To improve the accuracy of synchronization, a communications system component can have a timing device for measuring the time difference between a request for and reception of time information, and a delay time determination device for ascertaining an estimate of the delay time for the time information from the time

information transmitter to the communications system component on the basis of the measured time difference. Assuming that the delay time for the request approximately matches the delay time for the time information, the delay time for the time information is then found to be half the measured time difference. The accuracy of the

5 estimate of the delay time for time information can be increased by determining the estimate from a mean value for time differences measured for a number of requests, or variables derived from the time differences. This makes it possible to compensate for delay time fluctuations for the data transmitted via the communications network.

Accordingly, the comparison device in the communications system component can be
10 designed such that the ascertained estimate of the delay time for the time information is taken into account in the comparison result; e.g., by correcting the time information or the time value indicated by the real time clock.

The frequency with which time information is requested by a communications system component can be dependent on various criteria, such as the accuracy of the

15 clock generator, the range of variation of the time differences measured between a request for and reception of time information and/or the magnitude of a clock generator error ascertained upon prior adjustment of the clock generator. Preferably, the time request device can be designed such that time information is requested more frequently the less accurate the clock generator and the greater the range of variation of

20 the measured time differences or of the ascertained clock generator error.

In accordance with another advantageous embodiment of the present invention, a communications system component can have an input buffer, operating on the basis of the continuity principle (“first-in-first-out”, FIFO), for buffering a data stream received via the communications network. In this case, the input buffer is coupled to

25 the clock generator such that data elements in a buffered data stream are read with timing determined by the clock generator. The input buffer also has a filling level detection device coupled to it which can be used to detect the filling level of the input buffer.

A clock frequency controller therefore can be used to readjust the clock frequency of the clock generator on the basis of the detected filling level. Assuming that, at least when averaged over time, the data stream received via the communications network is sent using a data rate prescribed by a clock generator in a

respective data stream transmitter, this allows the clock generator in the communications system component to be synchronized with the clock generator in the data stream transmitter when averaged over time. To compensate for brief delay time fluctuations for data elements in the data stream, an integration element can be
5 provided which is used to supply a clock frequency regulation signal derived from the filling level to the clock generator.

For the purposes of clock frequency regulation, a data stream of communications data received via the communications network, such as voice data, preferably can be used. Since communications data, particularly voice data, are frequently transmitted at a precisely maintained transmission rate oriented to the timing of the transmitter for the communications data when a connection has been set up, the clock frequency of the clock generator can be stabilized fairly precisely using received communications or voice data.
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In accordance with another advantageous embodiment of the present invention, the time information transmitter can have a detector device which can be used to ascertain a temporarily low transmission volume, such as for user and/or signaling data, in the communications network. A transmission controller in the time information transmitter then can be used to trigger transmission of time information when the ascertained transmission volume falls below a prescribed limit.
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20 Additional features and advantages of the present invention are described in, and will be apparent from, the following Detailed Description of the Invention and the Figures.

BRIEF DESCRIPTION OF THE FIGURES

Figure 1 shows a schematic illustration of a communications system with an exchange distributed over a communications network.
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Figure 2 shows a schematic illustration of a terminal connection group for the distributed exchange.

DETAILED DESCRIPTION OF THE INVENTION

Figure 1 shows, schematically, a communications system with an exchange
30 PBX distributed over a communications network KN and terminals EA1,...,EAN, and EB1,...,EBN connected to the exchange. In this arrangement, the exchange PBX has a central controller ZS and terminal connection groups EAGA and EAGB as

communications system components coupled via the communications network KN. The terminal connection groups belong to the “peripheral section” of the exchange PBX. The terminal connection group EAGA couples the terminals EA1,...,EAN to the exchange PBX, and the terminal connection group EAGB couples the terminals EB1,...,EBN to the exchange PBX. The central controller ZS, for its part, has a time information transmitter ZIG having a reference real time clock RRTC. The reference real time clock RRTC can be adjusted on the basis of world time information received from a satellite; for example, using a GPS (global positioning system) receiver.

The communications network KN, which can be in the form of a local area network (LAN) or an internet protocol based network, for example, can have not only the communications system components ZS, EAGA, EAGB but also data processing devices (not shown) coupled to it. A communications network in the form of a local area network (LAN) or internet protocol based network very easily can be extended and have other communications and/or data processing devices added to it and, hence, can be matched very flexibly even to a great diversity of requirements. In the present exemplary embodiment, the communications network KN is used for transmitting both any communications data and any control data between the terminal connection groups EAGA, EAGB and the central controller ZS.

In the present exemplary embodiment, an existing connection is used to transmit communications data KD, such as voice data, from the terminal EB1 to the terminal EA1 via the terminal connection group EAGA, the communications network KN and the terminal connection group EAGB. Setup of the connection previously has been prompted by the central controller ZS by virtue of the terminal connection groups EAGA, EAGB being sent respective address information, among other things, which identifies the respective other terminal connection group in the communications network KN. Accordingly, the terminal connection group EAGB provides the communications data KD to be transmitted with the address information identifying the terminal connection group EAGA, and the communications data KD are thus transmitted via the communications network KN to the terminal connection group EAGA, which finally forwards the communications data KD to the terminal EA1.

To synchronize the terminal connection groups EAGA and EAGB with one another, each of the terminal connection groups EAGA and EAGB is independently

synchronized with the time information transmitter ZIG in the central controller ZS. In this case, synchronization takes place over the communications network KN. To this end, the terminal connection groups EAGA and EAGB send a respective time request message ZA1 or ZA2, such as on the basis of the “network time protocol” (NTP), to 5 the time information transmitter ZIG via the communications network KN. The received time request messages ZA1, ZA2 prompt the time information transmitter to request respective current time information ZI1 or ZI2 from the reference real time clock RRTC and then to transmit it, having been provided with address information which identifies the terminal connection group EAGA or EAGB, to the respectively 10 addressed terminal connection group EAGA or EAGB via the communications network KN.

Figure 2 shows a more detailed illustration of the terminal connection group EAGA. The terminal connection group EAGA, which is coupled to the communications network KN via a network interface NS, has a reception device EE, 15 an input buffer EP, a real time clock RTC, a clock generator ZTG, two clock frequency controllers TS1 and TS2 and a terminal interface EGS as further functional components. The terminal interface EGS, which can be in the form of a series of S0 interfaces based on the ISDN standard, for example, connects the terminals EA1,...,EAN. The clock frequency controller TS1, for its part, has a comparison 20 device VE, a delay time determination device LB, and an integration element IG. For reasons of clarity, other functional components of the terminal connection group EAGA which make no direct contribution to understanding the present invention have not been shown. The functional components which are shown each can be produced using software modules running on a system processor in the terminal connection 25 group EAGA.

The clock generator ZTG, which, by way of example, can be in the form of a “TCXO” (temperature compensated x-tal oscillator), an “OCXO” (oven controlled x-tal oscillator) or a “TCVCXO” (temperature compensated voltage controlled x-tal oscillator), provides a clock signal T which is supplied to the real time clock RTC, to 30 the input buffer EP and to the terminal interface EGS in order to control timing. The frequency of the clock signal T from the clock generator ZTG can be regulated within prescribed limits in this case. The clock signal T forms both the time base for the real

time clock RTC and the time base for the data rate at which the communications data KD are transmitted via the terminal interface EGS; e.g., 64 kbit/s for an ISDN base channel.

To synchronize the clock generator ZTG with the time dimension for the time information transmitter ZIG, the clock frequency controller TS1 uses the network interface NS to send the time request message ZA1 to the time information transmitter ZIG via the communications network KN. The transmission time stored for the time request message ZA1 is a current time value indicated by the real time clock RTC. The time request message ZA1 prompts the time information transmitter ZIG, as already stated above, to transmit the time information ZI1 to the terminal connection group EAGA via the communications network KN. The network interface NS in the terminal connection group EAGA forwards the time information ZI1 to the reception device EE, where the time information ZI1 is extracted from a data stream which has been received via the communications network KN and also contains the communications data KD. The time information ZI1 is extracted in the reception device EE using a network protocol software module NP which is implemented for this purpose and which recognizes the time information ZI1 on the basis of identification information identifying time information. This can be done, by way of example, on the basis of the “network time protocol” (NTP) or the “digital time synchronization protocol” (DTSS). The extracted time information ZI1 is forwarded by the reception device EE to the clock frequency controller TS1, which determines the reception time for the time information ZI1 as the current time value ZR indicated by the real time clock RTC and evaluates the time information content of the time information ZI1. If the terminal connection group EAGA is governed by a local time, the time information content of the time information ZI1 can be converted to the locally used time; e.g., using stored tables. Such conversion may be necessary, for example, when the terminal connection group EAGA and the time information transmitter ZIG are in different time zones or are oriented to different reference times, such as GPS time (global positioning system) and UTC time (universal time coordinated).

The delay time determination device LB furthermore estimates the delay time for the time information ZI1 in the communications network KN as half the time

difference between the ascertained reception time ZR for the time information ZI1 and the stored transmission time for the time request message ZA1. To increase the accuracy of delay time determination and to compensate for brief delay time fluctuations in the communications network KN, the value obtained for the delay time
5 is averaged with values determined earlier for the delay time. Preferably, a sliding mean value is determined. If appropriate, a time stamp for the time information ZI1 also can be included in the delay time determination.

The time indicated by the time information content of the time information ZI1, and possibly matched to the locally used time, and also the value determined for the delay time are then supplied to the comparison device VE. The comparison device VE corrects the transmitted time by the value obtained for the delay time for the time information ZI1; e.g., by adding the two variables. The comparison device VE then compares the correct time with the time ZR indicated by the real time clock RTC for the reception time of the time information ZI1. Depending on the comparison result, a
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frequency regulation signal FRS is then formed to control the clock frequency of the clock generator ZTG. If the time indicated by the real time clock RTC is ahead of the corrected time derived from the time information ZI1, this involves forming a frequency regulation signal FRS for reducing the clock frequency of the clock generator ZTG. Accordingly, if the real time clock RTC is behind, a frequency
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regulation signal FRS for increasing the clock frequency is produced. The frequency regulation signal FRS is output by the clock frequency controller TS1 via the time-based integration element IG, whose time constant is proportioned such that delay time fluctuations typically arising in the communications network KN are compensated for. Preferably, if comparatively large discrepancies arise between the real time clock RTC
20

and the time derived from the time information ZI1, the clock frequency controller TS1 can request time information from the time information transmitter ZIG at relatively short time intervals. In addition, a maximum discrepancy between the real time clock RTC and a time derived from received time information can be prescribed and, if this is exceeded, the real time clock RTC is readjusted directly; i.e., by altering
25 the time it indicates.

In the time intervals between respective reception of time information, the clock frequency of the clock generator ZTG is stabilized using the communications

data KD likewise received via the communications network KN. To this end, the communications data KD are supplied to the input of the input buffer EP by the receiver device EE. The input buffer is in the form of a “first-in-first-out memory” from which buffered data are read in the order of time in which they were stored. A 5 first-in-first-out memory is frequently also referred to as a FIFO. The communications data KD buffered in the input buffer EP are read therefrom as stipulated by the clock signal T supplied by the clock generator ZTG and are supplied to the terminal interface EGS. This is used for finally transmitting the communications data KD to the terminal EA1.

10 Generally, communications data, and particularly voice data with a constant data rate strictly oriented to the timing of the transmitter for the communications data, are sent. Despite any delay time fluctuations to which such communications data sent at a constant data rate are subject, these communications data arrive at a receiver at the same data rate, at least when averaged over time. It is thus possible to use the time 15 average for the data rate of received communications data to synchronize a receiver of these communications data with the timing of the transmitter.

In the present exemplary embodiment, the communications data KD sent at a constant data rate from the terminal connection group EAGB to the terminal connection group EAGA via the communications network KN are used to stabilize the 20 clock frequency of the clock generator ZTG in the terminal connection group EAGA during the time interval between individual requests for time information. For this purpose, the terminal connection group EAGA detects the respectively current filling level of the input buffer EP (i.e., the limit up to which the input buffer EP is filled with communications data KD), at regular time intervals, and this filling level is transmitted 25 to the clock frequency controller TS2 in the form of filling level information FI. On the basis of the filling level information FI, the clock frequency controller TS2 forms a frequency regulation signal FRS which is output via an integration element IG and is combined with the frequency regulation signal formed by the clock frequency controller TS1 in order to regulate the clock frequency of the clock generator ZTG. 30 The time constant of the integration element IG in the clock frequency controller TS2 is proportioned such that delay time fluctuations typically arising in the communications network KN for the communications data KD are compensated for.

The integration elements IG in the clock frequency controllers TS1 and TS2 can be produced, by way of example, using a digital circuit for forming sliding mean values. If the filling level of the input buffer EP is above average, the clock frequency controller TS2 forms a frequency regulation signal FRS for increasing the clock frequency of the clock generator ZTG, whereas if the filling level is below average, a frequency regulation signal for reducing the clock frequency is formed. The frequency regulation signals FRS formed by the clock controllers TS1 and TS2 each can be supplied to the clock generator ZTG in a form combined with prescribed weighting factors. In this context, the frequency regulation signal formed by the clock frequency controller TS1 is preferably given a higher weighting than that formed by the clock frequency controller TS2. Due to the additional stabilization of the clock frequency of the clock generator ZTG on the basis of the filling level of the input buffer EP, it is possible to ensure synchronism between the terminal connection groups EAGA and EAGB even during comparatively long time intervals between individual time requests.

So that a prescribed accuracy of synchronization for the terminal connection groups EAGA and EAGB is also ensured across relatively large communications networks KN, network elements in the communications network, such as "repeaters" and/or "routers", can be arranged such that the respective number of network elements connected between the time information transmitter ZIG and the respective terminal connection group EAGA or EAGB and of network elements connected between the terminal connection groups EAGA and EAGB does not exceed a respectively prescribed number.

Although the present invention has been described with reference to specific embodiments, those of skill in the art will recognize that changes may be made thereto without departing from the spirit and scope of the invention as set forth in the hereafter appended claims.

ABSTRACT OF THE DISCLOSURE

To synchronize communications system components coupled via a communications network, a time information transmitter is provided which is used to transmit time information, possibly on request, relating to the communications system components. The communications system components each have a clock generator, which needs to be synchronized, and a real time clock, where the clock generator both determines the transmission rate for communications data which are to be transmitted and prescribes the time base for the real time clock. In addition, the communications system components each have a comparison device for comparing received time information with a current time value indicated by the real time clock, and a clock frequency controller for regulating the clock frequency of the clock generator on the basis of the comparison result.

In the claims:

On page 15, cancel line 1, and substitute the following left-hand justified heading therefor:

CLAIMS

5 Please cancel 1-14, without prejudice, and substitute the following claims therefor:

15. A system for synchronizing communications system components coupled via a communications network, comprising:

10 a time information transmitter for transmitting time information relating to the communications system components;

15 a time information reception device in each system component for receiving time information from the time information transmitter;

20 a clock generator in each system component with a controllable clock frequency for prescribing a transmission data rate for communication data which are to be transmitted;

25 a real time clock in each system component whose timing is controlled by the clock generator;

30 a comparison device in each system component for comparing received time information with a current time value indicated by the real time clock;

a clock frequency controller in each system component for controlling the clock frequency of the clock generator based on a comparison result from the comparison device;

35 an input buffer in each system component for buffering a data stream received via the communications network, where reading of data elements in the data stream from the input buffer is determined by the clock frequency of the clock generator;

40 a filling level detection device in each system component for detecting a filling level of the input buffer; and

45 a clock frequency controller in each system component for readjusting the clock frequency of the clock generator based on the detected filling level.

16. A system for synchronizing communications system components as claimed in Claim 15, wherein the clock generator in a system component is one of temperature stabilized and temperature compensated.

5 17. A system for synchronizing communications system components as claimed in Claim 15, wherein the clock generator in a system component is produced by a main oscillator outputting a timing pulse and by a reference oscillator which regulates the clock frequency of the main oscillator and otherwise freewheels, the reference oscillator being one of temperature stabilized and temperature compensated.

10 18. A system for synchronizing communications system components as claimed in Claim 15, wherein the time information reception device in a system component includes a radio reception device for wireless reception of time information from the time information transmitter.

15 19. A system for synchronizing communications system components as claimed in Claim 15, wherein the time information reception device in a system component is coupled to the communications network via a network interface, and may extract time information from a data stream transmitted to the system component via the communications network.

20 20. A system for synchronizing communications system components as claimed in Claim 19, further comprising a time request device in each system component for requesting time information from the time information transmitter.

25 21. A system for synchronizing communications system components as claimed in Claim 20, further comprising:

 a timing device in each system component for measuring a time difference between a request for and reception of time information;

30 a delay time determination device in each system component for ascertaining an estimate for the delay time for the time information from the time information

transmitter to the respective system component based on the measured time difference; and

5 a comparison device in each system component for comparing received time information with a current time value indicated by the real time clock, taking into account the estimated delay time.

22. A system for synchronizing communications system components as claimed in Claim 21, wherein the timing device is produced using the real time clock.

10 23. A system for synchronizing communications system components as claimed in Claim 21, further comprising a delay time determination device in each system component for ascertaining the estimate of the delay time based on one of averaging over a plurality of measured time differences and variables derived from the averaging.

15 24. A system for synchronizing communications system components as claimed in Claim 20, further comprising a time request device in each system component for requesting time information at time intervals which are dependent on how greatly the measured time differences vary.

20 25. A system for synchronizing communications system components as claimed in Claim 20, further comprising a time request device in each system component for requesting time information at time intervals which are dependent on the comparison result from the comparison device.

25 26. A system for synchronizing communications system components as claimed in Claim 15, wherein the data stream to be buffered includes communications user data received via the communications network.

30 27. A system for synchronizing communications system components as claimed in Claim 15, wherein the time information transmitter in a system component includes a detector device for ascertaining a temporarily low transmission volume in

the communications network and a transmission controller for triggering transmission of time information when a low transmission volume is ascertained.

28. A system for synchronizing communications system components as
5 claimed in Claim 15, further comprising a PLL circuit in each system component for
regulating the clock frequency of the clock generator.

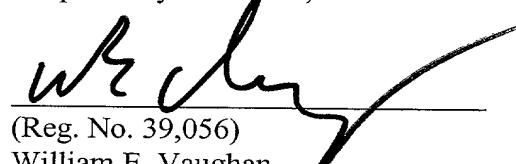
REMARKS

10 The present amendment makes editorial changes and corrects typographical errors in the specification, which includes the Abstract, in order to conform the specification to the requirements of United States Patent Practice. No new matter is added thereby. Attached hereto is a marked-up version of the changes made to the specification by the present amendment. The attached page is captioned "Version With Markings To Show Changes Made".

15 In addition, the present amendment cancels original claims 1-14 in favor of new claims 15-28. Claims 15-28 have been presented solely because the revisions by red-lining and underlining which would have been necessary in claims 1-14 in order to present those claims in accordance with preferred United States Patent Practice would have been too extensive, and thus would have been too burdensome. The present
20 amendment is intended for clarification purposes only and not for substantial reasons related to patentability pursuant to 35 U.S.C. §§101, 102, 103 or 112. Indeed, the cancellation of claims 1-14 does not constitute an intent on the part of the Applicants to surrender any of the subject matter of claims 1-14.

Early consideration on the merits is respectfully requested.

25 Respectfully submitted,


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VERSIONS WITH MARKINGS TO SHOW CHANGES MADE

In The Specification:

The Specification of the present application, including the Abstract, has been amended as follows:

5 **Description**

SPECIFICATION

TITLE OF THE INVENTION

SYSTEM ARRANGEMENT FOR SYNCHRONIZING COMMUNICATIONS

SYSTEM COMPONENTS COUPLED VIA A COMMUNICATIONS NETWORK

10 **BACKGROUND OF THE INVENTION**

In the course of current development taking place, communications systems and control thereof are becoming increasingly decentralized. In this regard, a communications system is split into individual subsystems coupled via a communications network, such as a local area network (LAN) or a network based on an internet protocol (IP). This allows, by way of example, components of a relatively large exchange to be distributed over a communications network.

Contemporary communications systems normally provide a large number of communications services and service features. For some of these communications services and service features, such as for "CBO services" (continuous bit stream operation), which include fax, modem, voice and video transmissions, it is necessary for the respective communications system components involved to be in sync for communications data which are to be transmitted.

Arrangements for synchronizing communications system components coupled via a communications network are known in communications networks using direct SDH (synchronous digital hierarchy) or PDH (plesiochronous digital hierarchy) based transmission; e.g.; from section 8 of ITU-T recommendation G.803 and the references indicated therein. In this case, a reference clock is transmitted to the communications system components which are to be synchronized on the physical layer of the transmission protocol used. However, transmitting a reference clock in the physical layer requires continuous layer 1 connections to the individual communications system components. This means that As such, relatively complex communications network

structures can be produced only with a great deal of effort, however. In communications networks which can be configured more flexibly, such as local area networks (LAN) or internet protocol based networks, continuous layer 1 connections are generally are not provided.

5 It is an object of the present invention to specify an arrangement a system which is more flexible than the prior art in order to synchronize communications system components coupled via a communications network for communications data which are to be transmitted.

10 ~~This object is achieved by an arrangement having the features of patent claim 1.~~

SUMMARY OF THE INVENTION

15 To Accordingly, pursuant to the present invention, to synchronize communications system components coupled via a communications network, they are sent time information from a time information transmitter. The communications system components' orienting of a respectively dedicated time dimension to respective time information received from the time information transmitter synchronizes these communications system components with one another.

20 A communications system component is synchronized by readjusting the clock frequency of a clock generator intended to prescribe the transmission data rate for communications data whose transmission involves the communications system component in question. In this case, the clock frequency is readjusted by comparing received time information with a current time value on a real time clock whose timing is controlled, in accordance with the present invention, by a timing pulse from the clock generator actually provided for prescribing the transmission data rate for communications data which are to be transmitted. The readjustment of the clock 25 generator's clock frequency is thus indirectly used to adjust the real time clock itself on the basis of the time information received. This indirect adjustment prevents abrupt changes in the time indicated by the real time clock and attenuates effects of delay time fluctuations in received time information.

30 The inventive arrangement system is largely independent of the type of communications network coupling the communications system components. Thus, by way of example, the communications network used can be a "local area network" (LAN) or an internet protocol based communications network.

~~Advantageous embodiments and developments of the invention are specified in the subclaims.~~

In accordance with one advantageous embodiment of the present invention, the clock generator in a communications system component can be temperature stabilized or temperature compensated. To increase the clock accuracy, the clock generator ~~can~~ also can be produced on the basis of the “2-oscillator concept”. In this context, a main oscillator and a temperature stabilized or temperature compensated reference oscillator which regulates the clock frequency of the main oscillator and otherwise freewheels is provided. The more accurate the clock generator in a communications system component, the longer the time intervals for which the communications system component in question remains in sync even without receiving time information.

In accordance with another advantageous embodiment of the present invention, a communications system component can be sent the time information wirelessly, e.g., from a GPS satellite (global positioning system), from a time signal transmitter, such as DCF77, or from a time information transmitter associated with the communications system. For this purpose, the time information reception device in the communications system component in question has a radio reception device for wirelessly receiving the time information. ~~On account of~~ Due to the very short delay time for time information transmitted by radio, this allows very accurate synchronization to be achieved.

Alternatively, a communications system component ~~can~~ also can be sent time information via the communications network from a time information transmitter, e.g. such as in the form of a time information server, which is likewise coupled to the communications network. ~~This means that~~ As such, an existing network infrastructure ~~can~~ also can be used for synchronizing the communications system components. This alternative allows a complex radio reception device to be dispensed with in the communications system components which are to be synchronized. Instead, time information ~~can~~ easily can be received by coupling the time information reception device in a communications system component to the communications network via a network interface and providing it with ~~means for extracting the ability to extract~~ time information from a data stream transmitted via the communications network.

In accordance with one advantageous development embodiment of the present invention, a communications system component can have a time request device so that it can request time information from the time information transmitter via the communications network. In this case, the request can be made preferably using 5 known network protocols, such as the “network time protocol” (NTP) or the “digital time synchronization protocol” (DTSS).

To improve the accuracy of synchronization, a communications system component can have a timing device for measuring the time difference between a request for and reception of time information, and a delay time determination device 10 for ascertaining an estimate of the delay time for the time information from the time information transmitter to the communications system component on the basis of the measured time difference. Assuming that the delay time for the request approximately matches the delay time for the time information, the delay time for the time information is then found to be half the measured time difference. The accuracy of the 15 estimate of the delay time for time information can be increased by determining the estimate from a mean value for time differences measured for a plurality number of requests, or variables derived from said the time differences. This makes it possible to compensate for delay time fluctuations for the data transmitted via the communications network. The Accordingly, the comparison device in the communications system 20 component can accordingly be designed such that the ascertained estimate of the delay time for the time information is taken into account in the comparison result; e.g., by correcting the time information or the time value indicated by the real time clock.

The frequency with which time information is requested by a communications system component can be dependent on various criteria, such as the accuracy of the 25 clock generator, the range of variation of the time differences measured between a request for and reception of time information and/or the magnitude of a clock generator error ascertained upon prior adjustment of the clock generator. Preferably, the time request device can be designed such that time information is requested more frequently the less accurate the clock generator and the greater the range of variation of 30 the measured time differences or of the ascertained clock generator error.

In accordance with another advantageous development embodiment of the present invention, a communications system component can have an input buffer,

operating on the basis of the continuity principle ("first-in-first-out", FIFO), for buffering a data stream received via the communications network. In this case, the input buffer is coupled to the clock generator such that data elements in a buffered data stream are read with timing determined by the clock generator. The input buffer also 5 has a filling level detection device coupled to it which can be used to detect the filling level of the input buffer.

A clock frequency controller ~~can~~ therefore can be used to readjust the clock frequency of the clock generator on the basis of the detected filling level. Assuming that, at least when averaged over time, the data stream received via the 10 communications network is sent using a data rate prescribed by a clock generator in a respective data stream transmitter, this allows the clock generator in the communications system component to be synchronized with the clock generator in the data stream transmitter when averaged over time. To compensate for brief delay time fluctuations for data elements in the data stream, an integration element can be 15 provided which is used to supply a clock frequency regulation signal derived from the filling level to the clock generator.

For the purposes of clock frequency regulation, a data stream of communications data received via the communications network, such as voice data, ~~can~~ preferably can be used. Since communications data, and particularly voice data, 20 are frequently transmitted at a precisely maintained transmission rate oriented to the timing of the transmitter for the communications data when a connection has been set up, the clock frequency of the clock generator can be stabilized ~~particularly~~ fairly precisely using received communications or voice data.

In accordance with another advantageous ~~development~~ embodiment of the 25 present invention, the time information transmitter can have a detector device which can be used to ascertain a temporarily low transmission volume, ~~e.g.~~ such as for user and/or signaling data, in the communications network. A transmission controller in the time information transmitter ~~can~~ then can be used to trigger transmission of time information when the ascertained transmission volume falls below a prescribed limit.

30 ~~An exemplary embodiment of the invention is explained in more detail below with reference to the drawing. Additional features and advantages of the present~~

invention are described in, and will be apparent from, the following Detailed Description of the Invention and the Figures.

In the drawing,

BRIEF DESCRIPTION OF THE FIGURES

5 ~~Figure 1 shows a schematic illustration of a communications system with an exchange distributed over a communications network, and,~~

~~Figure 2 shows a schematic illustration of a terminal connection group for the distributed exchange.~~

DETAILED DESCRIPTION OF THE INVENTION

10 Figure 1 shows, schematically, a communications system with an exchange PBX distributed over a communications network KN and terminals EA1,...,EAN, and EB1,...,EBN connected to ~~said~~ the exchange. In this arrangement, the exchange PBX has a central controller ZS and terminal connection groups EAGA and EAGB as communications system components coupled via the communications network KN.

15 The terminal connection groups belong to the “peripheral section” of the exchange PBX. The terminal connection group EAGA couples the terminals EA1,...,EAN to the exchange PBX, and the terminal connection group EAGB couples the terminals EB1,...,EBN to the exchange PBX. The central controller ZS, for its part, has a time information transmitter ZIG having a reference real time clock RRTC. The reference
20 real time clock RRTC can be adjusted on the basis of world time information received from a satellite; for example, using a GPS (global positioning system) receiver.

The communications network KN, which can be in the form of a local area network (LAN) or an internet protocol based network, for example, can have not only the communications system components ZS, EAGA, EAGB but also data processing devices (not shown) coupled to it. A communications network in the form of a local area network (LAN) or internet protocol based network ~~can~~ very easily can be extended and have other communications and/or data processing devices added to it, and, hence, can be matched very flexibly even to a great diversity of requirements. In the present exemplary embodiment, the communications network KN is used for
30 transmitting both any communications data and any control data between the terminal connection groups EAGA, EAGB and the central controller ZS.

In the present exemplary embodiment, an existing connection is used to transmit communications data KD, such as voice data, from the terminal EB1 to the terminal EA1 via the terminal connection group EAGA, the communications network KN and the terminal connection group EAGB. Setup of the connection has previously 5 been prompted by the central controller ZS by virtue of the terminal connection groups EAGA, EAGB being sent respective address information, among other things, which identifies the respective other terminal connection group in the communications network KN. Accordingly, the terminal connection group EAGB provides the communications data KD to be transmitted with the address information identifying the terminal connection group EAGA, and the communications data KD are thus transmitted via the communications network KN to the terminal connection group EAGA, which finally forwards the communications data KD to the terminal EA1.

To synchronize the terminal connection groups EAGA and EAGB with one another, each of the terminal connection groups EAGA and EAGB is independently 10 synchronized with the time information transmitter ZIG in the central controller ZS. In this case, synchronization takes place over the communications network KN. To this end, the terminal connection groups EAGA and EAGB send a respective time request message ZA1 or ZA2, e.g. such as on the basis of the “network time protocol” (NTP), to the time information transmitter ZIG via the communications network KN. The 15 received time request messages ZA1, ZA2 prompt the time information transmitter to request respective current time information ZI1 or ZI2 from the reference real time clock RRTC and then to transmit it, having been provided with address information which identifies the terminal connection group EAGA or EAGB, to the respectively 20 addressed terminal connection group EAGA or EAGB via the communications network KN.

Figure 2 shows a more detailed illustration of the terminal connection group EAGA. The terminal connection group EAGA, which is coupled to the communications network KN via a network interface NS, has a reception device EE, an input buffer EP, a real time clock RTC, a clock generator ZTG, two clock frequency 25 controllers TS1 and TS2 and a terminal interface EGS as further functional components. The terminal interface EGS, which can be in the form of a series of S0 interfaces based on the ISDN standard, for example, connects the terminals

EA1,...,EAN. The clock frequency controller TS1, for its part, has a comparison device VE, a delay time determination device LB, and an integration element IG. For reasons of clarity, other functional components of the terminal connection group EAGA which make no direct contribution to understanding the present invention have 5 not been shown. The functional components which are shown ~~can~~ each can be produced using software modules running on a system processor in the terminal connection group EAGA.

The clock generator ZTG, which, by way of example, can be in the form of a 10 "TCXO" (temperature compensated x-tal oscillator), an "OCXO" (oven controlled x-tal oscillator) or a "TCVCXO" (temperature compensated voltage controlled x-tal oscillator), provides a clock signal T which is supplied to the real time clock RTC, to the input buffer EP and to the terminal interface EGS in order to control timing. The frequency of the clock signal T from the clock generator ZTG can be regulated within prescribed limits in this case. The clock signal T forms both the time base for the real 15 time clock RTC and the time base for the data rate at which the communications data KD are transmitted via the terminal interface EGS; e.g., 64 kbit/s for an ISDN base channel.

To synchronize the clock generator ZTG with the time dimension for the time 20 information transmitter ZIG, the clock frequency controller TS1 uses the network interface NS to send the time request message ZA1 to the time information transmitter ZIG via the communications network KN. The transmission time stored for the time request message ZA1 is a current time value indicated by the real time clock RTC. The time request message ZA1 prompts the time information transmitter ZIG, as 25 already stated above, to transmit the time information ZI1 to the terminal connection group EAGA via the communications network KN. The network interface NS in the terminal connection group EAGA forwards the time information ZI1 to the reception device EE, where the time information ZI1 is extracted from a data stream which has been received via the communications network KN and also contains the communications data KD. The time information ZI1 is extracted in the reception 30 device EE using a network protocol software module NP which is implemented for this purpose and which recognizes the time information ZI1 on the basis of identification information identifying time information. This can be done, by way of

example, on the basis of the “network time protocol” (NTP) or the “digital time synchronization protocol” (DTSS). The extracted time information ZI1 is forwarded by the reception device EE to the clock frequency controller TS1, which determines the reception time for the time information ZI1 as the current time value ZR indicated
5 by the real time clock RTC and evaluates the time information content of the time information ZI1. If the terminal connection group EAGA is governed by a local time, the time information content of the time information ZI1 can be converted to the locally used time; e.g., using stored tables. Such conversion may be necessary, for example, when the terminal connection group EAGA and the time information transmitter ZIG are in different time zones or are oriented to different reference times, such as GPS time (global positioning system) and UTC time (universal time coordinated).

10 The delay time determination device LB furthermore estimates the delay time for the time information ZI1 in the communications network KN as half the time difference between the ascertained reception time ZR for the time information ZI1 and the stored transmission time for the time request message ZA1. To increase the accuracy of delay time determination and to compensate for brief delay time fluctuations in the communications network KN, the value obtained for the delay time is averaged with values determined earlier for the delay time. Preferably, a sliding
15 mean value is determined. If appropriate, a time stamp for the time information ZI1
20 ~~can also~~ can be included in the delay time determination.

25 The time indicated by the time information content of the time information ZI1, and possibly matched to the locally used time, and also the value determined for the delay time are then supplied to the comparison device VE. The comparison device VE corrects the transmitted time by the value obtained for the delay time for the time information ZI1; e.g., by adding the two variables. The comparison device VE then compares the correct time with the time ZR indicated by the real time clock RTC for the reception time of the time information ZI1. Depending on the comparison result, a frequency regulation signal FRS is then formed to control the clock frequency of the
30 clock generator ZTG. If the time indicated by the real time clock RTC is ahead of the corrected time derived from the time information ZI1, this involves forming a frequency regulation signal FRS for reducing the clock frequency of the clock

generator ZTG. Accordingly, if the real time clock RTC is behind, a frequency regulation signal FRS for increasing the clock frequency is produced. The frequency regulation signal FRS is output by the clock frequency controller TS1 via the time-based integration element IG, whose time constant is proportioned such that delay time fluctuations typically arising in the communications network KN are compensated for. Preferably, if comparatively large discrepancies arise between the real time clock RTC and the time derived from the time information ZI1, the clock frequency controller TS1 can request time information from the time information transmitter ZIG at relatively short time intervals. In addition, a maximum discrepancy between the real time clock RTC and a time derived from received time information can be prescribed, and, if this is exceeded, the real time clock RTC is readjusted directly; i.e., by altering the time it indicates.

In the time intervals between respective reception of time information, the clock frequency of the clock generator ZTG is stabilized using the communications data KD likewise received via the communications network KN. To this end, the communications data KD are supplied to the input of the input buffer EP by the receiver device EE. The input buffer is in the form of a “first-in-first-out memory” from which buffered data are read in the order of time in which they were stored. A first-in-first-out memory is frequently also referred to as a FIFO. The communications data KD buffered in the input buffer EP are read therefrom as stipulated by the clock signal T supplied by the clock generator ZTG and are supplied to the terminal interface EGS. This is used for finally transmitting the communications data KD to the terminal EA1.

Generally, communications data, and particularly voice data with a constant data rate strictly oriented to the timing of the transmitter for the communications data, are sent. Despite any delay time fluctuations to which such communications data sent at a constant data rate are subject, these communications data arrive at a receiver at the same data rate, at least when averaged over time. It is thus possible to use the time average for the data rate of received communications data to synchronize a receiver of these communications data with the timing of the transmitter.

In the present exemplary embodiment, the communications data KD sent at a constant data rate from the terminal connection group EAGB to the terminal

connection group EAGA via the communications network KN are used to stabilize the clock frequency of the clock generator ZTG in the terminal connection group EAGA during the time interval between individual requests for time information. For this purpose, the terminal connection group EAGA detects the respectively current filling 5 level of the input buffer EP, i.e. (i.e., the limit up to which the input buffer EP is filled with communications data KD), at regular time intervals, and this filling level is transmitted to the clock frequency controller TS2 in the form of filling level information FI. On the basis of the filling level information FI, the clock frequency controller TS2 forms a frequency regulation signal FRS which is output via an 10 integration element IG and is combined with the frequency regulation signal formed by the clock frequency controller TS1 in order to regulate the clock frequency of the clock generator ZTG. The time constant of the integration element IG in the clock frequency controller TS2 is proportioned such that delay time fluctuations typically arising in the communications network KN for the communications data KD are compensated for.

15 The integration elements IG in the clock frequency controllers TS1 and TS2 can be produced, by way of example, using a digital circuit for forming sliding mean values. If the filling level of the input buffer EP is above average, the clock frequency controller TS2 forms a frequency regulation signal FRS for increasing the clock frequency of the clock generator ZTG, whereas if the filling level is below average, a 20 frequency regulation signal for reducing the clock frequency is formed. The frequency regulation signals FRS formed by the clock controllers TS1 and TS2 ~~can~~ each can be supplied to the clock generator ZTG in a form combined with prescribed weighting factors. In this context, the frequency regulation signal formed by the clock frequency controller TS1 is preferably given a higher weighting than that formed by the clock 25 frequency controller TS2. On account of Due to the additional stabilization of the clock frequency of the clock generator ZTG on the basis of the filling level of the input buffer EP, it is possible to ensure synchronism between the terminal connection groups EAGA and EAGB even during comparatively long time intervals between individual time requests.

30 So that a prescribed accuracy of synchronization for the terminal connection groups EAGA and EAGB is also ensured across relatively large communications networks KN, network elements in the communications network, such as “repeaters”

and/or "routers", can be arranged such that the respective number of network elements connected between the time information transmitter ZIG and the respective terminal connection group EAGA or EAGB and of network elements connected between the terminal connection groups EAGA and EAGB does not exceed a respectively 5 prescribed number.

Although the present invention has been described with reference to specific embodiments, those of skill in the art will recognize that changes may be made thereto without departing from the spirit and scope of the invention as set forth in the hereafter appended claims.

Abstract

~~Arrangement for synchronizing communications system components coupled via a communications network~~

ABSTRACT OF THE DISCLOSURE

5 To synchronize communications system components (~~EAGA, EAGB~~) coupled via a communications network(~~KN~~), a time information transmitter (~~ZIG~~) is provided which is used to transmit time information(~~ZI1, ZI2~~), possibly on request-, relating to the communications system components(~~EAGA, EAGB~~). The communications system components each have a clock generator(~~ZTG~~), which needs to be

10 synchronized, and a real time clock(~~RTC~~), where the clock generator (~~ZTG~~) both determines the transmission rate for communications data (~~KD~~) which are to be transmitted and prescribes the time base for the real time clock(~~RTC~~). In addition, the communications system components (~~EAGA, EAGB~~) each have a comparison device (~~VE~~) for comparing received time information (~~ZI1, ZI2~~) with a current time value (~~ZR~~) indicated by the real time clock(~~RTC~~), and a clock frequency controller (~~TS1~~) for regulating the clock frequency of the clock generator (~~ZTG~~) on the basis of the comparison result.

15

FIGURE 2

GR 99 P 2795

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Description

Arrangement for synchronizing communications system components coupled via a communications network

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In the course of current development taking place, communications systems and control thereof are becoming increasingly decentralized. In this regard, a communications system is split into individual subsystems coupled via a communications network, such as a local area network (LAN) or a network based on an internet protocol (IP). This allows, by way of example, components of a relatively large exchange to be distributed over a communications network.

15

Contemporary communications systems normally provide a large number of communications services and service features. For some of these communications services and service features, such as for "CBO services" (continuous bit stream operation), which include fax, modem, voice and video transmissions, it is necessary for the respective communications system components involved to be in sync for communications data which are to be transmitted.

25

Arrangements for synchronizing communications system components coupled via a communications network are known in communications networks using direct SDH (synchronous digital hierarchy) or PDH (plesiochronous digital hierarchy) based transmission, e.g. from section 8 of ITU-T recommendation G.803 and the references indicated therein. In this case, a reference clock is transmitted to the communications system components which are to be synchronized on the physical layer of the transmission protocol used. However, transmitting a reference clock in the physical layer requires continuous layer 1 connections to the individual communications system components. This means

- 1a -

that relatively complex communications network structures can be produced only with a great deal of effort, however. In communications networks which can be configured more

flexibly, such as local area networks (LAN) or internet protocol based networks, continuous layer 1 connections are generally not provided.

5 It is an object of the present invention to specify an arrangement which is more flexible than the prior art in order to synchronize communications system components coupled via a communications network for communications data which are to be transmitted.

10

This object is achieved by an arrangement having the features of patent claim 1.

15 To synchronize communications system components coupled via a communications network, they are sent time information from a time information transmitter. The communications system components' orienting of a respectively dedicated time dimension to respective time information received from the time information transmitter synchronizes these communications system components with one another.

20

25 A communications system component is synchronized by readjusting the clock frequency of a clock generator intended to prescribe the transmission data rate for communications data whose transmission involves the communications system component in question. In this case, the clock frequency is readjusted by comparing received time information with a current time value on a real time clock whose timing is controlled, in accordance with the invention, by a timing pulse from the clock generator actually provided for prescribing the transmission data rate for communications data which are to be transmitted. The readjustment of the 30 clock generator's clock frequency is thus indirectly used to adjust the real time clock itself on the basis of the time information received. This indirect adjustment prevents abrupt changes in the time indicated by the real time

35

clock and attenuates effects of delay time fluctuations in received time information.

5 The inventive arrangement is largely independent of the type of communications network coupling the communications system components. Thus, by way of example, the communications network used can be a "local area network" (LAN) or an internet protocol based communications network.

10

Advantageous embodiments and developments of the invention are specified in the subclaims.

15 In accordance with one advantageous embodiment of the invention, the clock generator in a communications system component can be temperature stabilized or temperature compensated. To increase the clock accuracy, the clock generator can also be produced on the basis of the "2-oscillator concept". In this 20 context, a main oscillator and a temperature stabilized or temperature compensated reference oscillator which regulates the clock frequency of the main oscillator and otherwise freewheels is provided. The more accurate the clock generator in a communications system 25 component, the longer the time intervals for which the communications system component in question remains in sync even without receiving time information.

30 In accordance with another advantageous embodiment of the invention, a communications system component can be sent the time information wirelessly, e.g. from a GPS satellite (global positioning system), from a time signal transmitter, such as DCF77, or from a time information transmitter associated with the 35 communications system. For this purpose, the time information reception device in the communications system component in question has a radio reception device for wirelessly receiving the time information. On account of the very short delay time for time

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information transmitted by radio, this allows very accurate synchronization to be achieved.

Alternatively, a communications system component can also be sent time information via the communications network from a time information transmitter, e.g. in the form of a time information server, which is likewise coupled to the communications network. This means that an existing network infrastructure can also be used for synchronizing the communications system components. This alternative allows a complex radio reception device to be dispensed with in the communications system components which are to be synchronized. Instead, time information can easily be received by coupling the time information reception device in a communications system component to the communications network via a network interface and providing it with means for extracting time information from a data stream transmitted via the communications network.

In accordance with one advantageous development of the invention, a communications system component can have a time request device so that it can request time information from the time information transmitter via the communications network. In this case, the request can be made preferably using known network protocols, such as the "network time protocol" (NTP) or the "digital time synchronization protocol" (DTSS).

To improve the accuracy of synchronization, a communications system component can have a timing device for measuring the time difference between a request for and reception of time information, and a delay time determination device for ascertaining an estimate of the delay time for the time information from the time information transmitter to the communications system component on the basis of the measured time difference. Assuming that the delay time for the request approximately matches the delay time for the time information, the delay time for the time information is then found to be half the measured time

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difference. The accuracy of the

estimate of the delay time for time information can be increased by determining the estimate from a mean value for time differences measured for a plurality of requests, or variables derived from said time differences. This makes it possible to compensate for delay time fluctuations for the data transmitted via the communications network. The comparison device in the communications system component can accordingly be designed such that the ascertained estimate of the delay time for the time information is taken into account in the comparison result, e.g. by correcting the time information or the time value indicated by the real time clock.

15 The frequency with which time information is requested by a communications system component can be dependent on various criteria, such as the accuracy of the clock generator, the range of variation of the time differences measured between a request for and 20 reception of time information and/or the magnitude of a clock generator error ascertained upon prior adjustment of the clock generator. Preferably, the time request device can be designed such that time information is requested more frequently the less accurate the clock 25 generator and the greater the range of variation of the measured time differences or of the ascertained clock generator error.

30 In accordance with another advantageous development of the invention, a communications system component can have an input buffer, operating on the basis of the continuity principle ("first-in-first-out", FIFO), for buffering a data stream received via the communications network. In this case, the input buffer is coupled to 35 the clock generator such that data elements in a buffered data stream are read with timing determined by the clock generator. The input buffer also has a filling level detection device coupled to it which can be used to detect the filling level of the input buffer.

A clock frequency controller can therefore be used to readjust the clock frequency of the clock generator on the basis of the detected filling level. Assuming that, at least when averaged over time, the data stream received via the communications network is sent using a data rate prescribed by a clock generator in a respective data stream transmitter, this allows the clock generator in the communications system component to be synchronized with the clock generator in the data stream transmitter when averaged over time. To compensate for brief delay time fluctuations for data elements in the data stream, an integration element can be provided which is used to supply a clock frequency regulation signal derived from the filling level to the clock generator.

For the purposes of clock frequency regulation, a data stream of communications data received via the communications network, such as voice data, can preferably be used. Since communications data, and particularly voice data, are frequently transmitted at a precisely maintained transmission rate oriented to the timing of the transmitter for the communications data when a connection has been set up, the clock frequency of the clock generator can be stabilized particularly precisely using received communications or voice data.

In accordance with another advantageous development, the time information transmitter can have a detector device which can be used to ascertain a temporarily low transmission volume, e.g. for user and/or signaling data, in the communications network. A transmission controller in the time information transmitter can then be used to trigger transmission of time information when the ascertained transmission volume falls below a prescribed limit.

An exemplary embodiment of the invention is explained in more detail below with reference to the drawing.

In the drawing,

5 figure 1 shows a schematic illustration of a communications system with an exchange distributed over a communications network, and

10 figure 2 shows a schematic illustration of a terminal connection group for the distributed exchange.

15 Figure 1 shows, schematically, a communications system with an exchange PBX distributed over a communications network KN and terminals EA1,...,EAN, and EB1,...,EBN connected to said exchange. In this arrangement, the exchange PBX has a central controller ZS and terminal connection groups EAGA and EAGB as communications system components coupled via the communications network KN. The terminal connection groups belong to 20 the "peripheral section" of the exchange PBX. The terminal connection group EAGA couples the terminals EA1,...,EAN to the exchange PBX, and the terminal connection group EAGB couples the terminals EB1,...,EBN to the exchange PBX. The central controller ZS, for its 25 part, has a time information transmitter ZIG having a reference real time clock RRTC. The reference real time clock RRTC can be adjusted on the basis of world time information received from a satellite, for example using a GPS (global positioning system) receiver.

30 The communications network KN, which can be in the form of a local area network (LAN) or an internet protocol based network, for example, can have not only the communications system components ZS, EAGA, EAGB but 35 also data processing devices (not shown) coupled to it. A communications network in the form of a local area network (LAN) or internet protocol based network can very easily be extended and have other communications and/or data processing devices added to it, and hence

can be matched very flexibly even to a great diversity of requirements. In the present exemplary embodiment, the communications network KN is used for transmitting both

any communications data and any control data between the terminal connection groups EAGA, EAGB and the central controller ZS.

5 In the present exemplary embodiment, an existing connection is used to transmit communications data KD, such as voice data, from the terminal EB1 to the terminal EA1 via the terminal connection group EAGA, the communications network KN and the terminal connection group EAGB. Setup of the connection has previously been prompted by the central controller ZS by virtue of the terminal connection groups EAGA, EAGB being sent respective address information, among other things, which identifies the respective other terminal connection group in the communications network KN. Accordingly, the terminal connection group EAGB provides the communications data KD to be transmitted with the address information identifying the terminal connection group EAGA, and the communications data KD are thus transmitted via the communications network KN to the terminal connection group EAGA, which finally forwards the communications data KD to the terminal EA1.

25 To synchronize the terminal connection groups EAGA and EAGB with one another, each of the terminal connection groups EAGA and EAGB is independently synchronized with the time information transmitter ZIG in the central controller ZS. In this case, synchronization takes place over the communications network KN. To this end, the terminal connection groups EAGA and EAGB send a respective time request message ZA1 or ZA2, e.g. on the basis of the "network time protocol" (NTP), to the time information transmitter ZIG via the communications network KN. The received time request messages ZA1, ZA2 prompt the time information transmitter to request respective current time information ZI1 or ZI2 from the reference real time clock RRTC and then to transmit it, having been provided with address information which

identifies the terminal connection group EAGA or EAGB, to the respectively addressed terminal connection group EAGA or EAGB via the communications network KN.

Figure 2 shows a more detailed illustration of the terminal connection group EAGA. The terminal connection group EAGA, which is coupled to the communications network KN via a network interface NS, has a reception device EE, an input buffer EP, a real time clock RTC, a clock generator ZTG, two clock frequency controllers TS1 and TS2 and a terminal interface EGS as further functional components. The terminal interface EGS, which can be in the form of a series of S0 interfaces based on the ISDN standard, for example, connects the terminals EA1, ..., EAN. The clock frequency controller TS1, for its part, has a comparison device VE, a delay time determination device LB, and an integration element IG. For reasons of clarity, other functional components of the terminal connection group EAGA which make no direct contribution to understanding the invention have not been shown. The functional components which are shown can each be produced using software modules running on a system processor in the terminal connection group EAGA.

The clock generator ZTG, which, by way of example, can be in the form of a "TCXO" (temperature compensated x-tal oscillator), an "OCXO" (oven controlled x-tal oscillator) or a "TCVCXO" (temperature compensated voltage controlled x-tal oscillator), provides a clock signal T which is supplied to the real time clock RTC, to the input buffer EP and to the terminal interface EGS in order to control timing. The frequency of the clock signal T from the clock generator ZTG can be regulated within prescribed limits in this case. The clock signal T forms both the time base for the real time clock RTC and the time base for the data rate at which the communications data KD are transmitted via the terminal interface EGS - e.g. 64 kbit/s for an ISDN base channel.

To synchronize the clock generator ZTG with the time dimension for the time information transmitter ZIG, the clock frequency controller

TS1 uses the network interface NS to send the time request message ZA1 to the time information transmitter ZIG via the communications network KN. The transmission time stored for the time request message ZA1 is a
5 current time value indicated by the real time clock RTC. The time request message ZA1 prompts the time information transmitter ZIG, as already stated above, to transmit the time information ZI1 to the terminal connection group EAGA via the communications network
10 KN. The network interface NS in the terminal connection group EAGA forwards the time information ZI1 to the reception device EE, where the time information ZI1 is extracted from a data stream which has been received via the communications network KN and also contains the
15 communications data KD. The time information ZI1 is extracted in the reception device EE using a network protocol software module NP which is implemented for this purpose and which recognizes the time information ZI1 on the basis of identification information
20 identifying time information. This can be done, by way of example, on the basis of the "network time protocol" (NTP) or the "digital time synchronization protocol" (DTSS). The extracted time information ZI1 is forwarded by the reception device EE to the clock frequency
25 controller TS1, which determines the reception time for the time information ZI1 as the current time value ZR indicated by the real time clock RTC and evaluates the time information content of the time information ZI1. If the terminal connection group EAGA is governed by a
30 local time, the time information content of the time information ZI1 can be converted to the locally used time, e.g. using stored tables. Such conversion may be necessary, for example, when the terminal connection group EAGA and the time information transmitter ZIG are
35 in different time zones or are oriented to different reference times, such as GPS time (global positioning system) and UTC time (universal time coordinated).

The delay time determination device LB furthermore estimates the delay time for the time information ZI1 in the communications network KN as half the time difference between the ascertained reception time ZR 5 for the time information ZI1 and the stored transmission time for the time request message ZA1. To increase the accuracy of delay time determination and to compensate for brief delay time fluctuations in the communications network KN, the value obtained for the 10 delay time is averaged with values determined earlier for the delay time. Preferably, a sliding mean value is determined. If appropriate, a time stamp for the time information ZI1 can also be included in the delay time determination.

15

The time indicated by the time information content of the time information ZI1, and possibly matched to the locally used time, and also the value determined for the delay time are then supplied to the comparison 20 device VE. The comparison device VE corrects the transmitted time by the value obtained for the delay time for the time information ZI1, e.g. by adding the two variables. The comparison device VE then compares the correct time with the time ZR indicated by the real 25 time clock RTC for the reception time of the time information ZI1. Depending on the comparison result, a frequency regulation signal FRS is then formed to control the clock frequency of the clock generator ZTG. If the time indicated by the real time clock RTC is 30 ahead of the corrected time derived from the time information ZI1, this involves forming a frequency regulation signal FRS for reducing the clock frequency of the clock generator ZTG. Accordingly, if the real time clock RTC is behind, a frequency regulation signal 35 FRS for increasing the clock frequency is produced. The frequency regulation signal FRS is output by the clock frequency controller TS1 via the time-based integration element IG, whose time constant is proportioned such

that delay time fluctuations typically arising in the communications network KN are compensated for. Preferably, if comparatively large discrepancies arise between the real time clock RTC and

the time derived from the time information ZI1, the clock frequency controller TS1 can request time information from the time information transmitter ZIG at relatively short time intervals. In addition, a 5 maximum discrepancy between the real time clock RTC and a time derived from received time information can be prescribed, and if this is exceeded the real time clock RTC is readjusted directly, i.e. by altering the time it indicates.

10

In the time intervals between respective reception of time information, the clock frequency of the clock generator ZTG is stabilized using the communications data KD likewise received via the communications network KN. To this end, the communications data KD are supplied to the input of the input buffer EP by the receiver device EE. The input buffer is in the form of a "first-in-first-out memory" from which buffered data are read in the order of time in which they were 15 stored. A first-in-first-out memory is frequently also referred to as a FIFO. The communications data KD buffered in the input buffer EP are read therefrom as stipulated by the clock signal T supplied by the clock generator ZTG and are supplied to the terminal 20 interface EGS. This is used for finally transmitting 25 the communications data KD to the terminal EA1.

Generally, communications data, and particularly voice data with a constant data rate strictly oriented to the 30 timing of the transmitter for the communications data, are sent. Despite any delay time fluctuations to which such communications data sent at a constant data rate are subject, these communications data arrive at a receiver at the same data rate, at least when averaged 35 over time. It is thus possible to use the time average for the data rate of received communications data to synchronize a receiver of these communications data with the timing of the transmitter.

TIME CHARTS

In the present exemplary embodiment, the communications data KD sent at a constant data rate from the terminal connection group EAGB to the terminal connection group EAGA via the communications network KN are used to

5 stabilize the clock frequency of the clock generator ZTG in the terminal connection group EAGA during the time interval between individual requests for time information. For this purpose, the terminal connection group EAGA detects the respectively current filling

10 level of the input buffer EP, i.e. the limit up to which the input buffer EP is filled with communications data KD, at regular time intervals, and this filling level is transmitted to the clock frequency controller TS2 in the form of filling level information FI. On the

15 basis of the filling level information FI, the clock frequency controller TS2 forms a frequency regulation signal FRS which is output via an integration element IG and is combined with the frequency regulation signal formed by the clock frequency controller TS1 in order

20 to regulate the clock frequency of the clock generator ZTG. The time constant of the integration element IG in the clock frequency controller TS2 is proportioned such that delay time fluctuations typically arising in the communications network KN for the communications data

25 KD are compensated for. The integration elements IG in the clock frequency controllers TS1 and TS2 can be produced, by way of example, using a digital circuit for forming sliding mean values. If the filling level of the input buffer EP is above average, the clock

30 frequency controller TS2 forms a frequency regulation signal FRS for increasing the clock frequency of the clock generator ZTG, whereas if the filling level is below average, a frequency regulation signal for reducing the clock frequency is formed. The frequency

35 regulation signals FRS formed by the clock controllers TS1 and TS2 can each be supplied to the clock generator ZTG in a form combined with prescribed weighting factors. In this context, the frequency regulation signal formed by the clock frequency controller TS1 is

preferably given a higher weighting than that formed by the clock frequency controller TS2. On account of the additional stabilization

of the clock frequency of the clock generator ZTG on the basis of the filling level of the input buffer EP, it is possible to ensure synchronism between the terminal connection groups EAGA and EAGB even during 5 comparatively long time intervals between individual time requests.

So that a prescribed accuracy of synchronization for the terminal connection groups EAGA and EAGB is also 10 ensured across relatively large communications networks KN, network elements in the communications network, such as "repeaters" and/or "routers", can be arranged such that the respective number of network elements connected between the time information transmitter ZIG 15 and the respective terminal connection group EAGA or EAGB and of network elements connected between the terminal connection groups EAGA and EAGB does not exceed a respectively prescribed number.

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New patent claims

1. An arrangement for synchronizing communications system components (EAGA, EAGB) coupled via a 5 communications network (KN), having a time information transmitter (ZIG) for transmitting time information (ZI1, ZI2) relating to the communications system components (EAGA, EAGB), where the communications systems components (EAGA, EAGB) each 10 have
 - a time information reception device (EE) for receiving time information from the time information transmitter (ZIG),
 - a clock generator (ZTG) with a controllable clock 15 frequency for prescribing a transmission data rate for communication data (KD) which are to be transmitted,
 - a real time clock whose timing is controlled by the clock generator (ZTG),
 - a comparison device (VE) for comparing received 20 time information (ZI1, ZI2) with a current time value (ZR) indicated by the real time clock (RTC),
 - a clock frequency controller (TS1) for controlling the clock frequency of the clock generator (ZTG) on the basis of the comparison result from the comparison 25 device (VE),
 - an input buffer (EP) for buffering a data stream received via the communications network (KN), where reading of data elements in the data stream from the input buffer (EP) is determined by the clock frequency 30 of the clock generator (ZTG),
 - a filling level detection device for detecting the filling level of the input buffer (EP), and
 - a clock frequency controller (TS2) for readjusting the clock frequency of the clock generator (ZTG) on the 35 basis of the detected filling level.

2. The arrangement as claimed in claim 1, characterized

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in that the clock generator (ZTG) in a communications

system component (EAGA, EAGB) is temperature stabilized or temperature compensated.

3. The arrangement as claimed in claim 1 or 2,
5 characterized

in that the clock generator (ZTG) is produced by a main oscillator outputting the timing pulse (T) and by a temperature stabilized or temperature compensated reference oscillator which regulates the clock
10 frequency of the main oscillator and otherwise freewheels.

4. The arrangement as claimed in one of the preceding
claims,
15 characterized

in that the time information reception device (EE) in a communications system component (EAGA, EAGB) has a radio reception device for wireless reception of time information (ZI1, ZI2) from the time information
20 transmitter.

5. The arrangement as claimed in one of claims 1 to
3,
characterized
25 in that the time information reception device (EE) in a communications system component (EAGA, EAGB) is coupled to the communications network (KN) via a network interface (NS), and
has means (NP) for extracting time information (ZI1,
30 ZI2) from a data stream transmitted to the communications system component (EAGA, EAGB) via the communications network (KN).

6. The arrangement as claimed in claim 5,
35 characterized
in that the communications system components (EAGA,
EAGB) each have a time request device (TS1) for

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requesting time information from the time information transmitter (ZIG).

7. The arrangement as claimed in claim 6,
characterized

in that the communications system components (EAGA,
EAGB) each have

5 - a timing device for measuring the time difference
between a request for and reception of time information
(ZI1, ZI2),

10 - a delay time determination device (LB) for
ascertaining an estimate for the delay time for the
time information (ZI1, ZI2) from the time information
transmitter (ZIG) to the respective communications
system component on the basis of the measured time
difference, and

15 - a comparison device (VE) for comparing received
time information (ZI1, ZI2) with a current time value
(ZR) indicated by the real time clock (RTC), taking
into account the estimated delay time.

20 8. The arrangement as claimed in claim 7,
characterized

in that the timing device is produced using the real
time clock (RTC).

25 9. The arrangement as claimed in claim 7 or 8,
characterized by

a delay time determination device (LB) for ascertaining
the estimate of the delay time on the basis of
averaging over a plurality of measured time differences
or variables derived therefrom.

30

10. The arrangement as claimed in one of claims 6 to
9,

characterized by

35 a time request device (TS1) for requesting time
information (ZI1, ZI2) at time intervals which are
dependent on how greatly the measured time differences
vary.

11. The arrangement as claimed in one of claims 6 to 10,

characterized by

5 a time request device (TS1) for requesting time information (ZI1, ZI2) at time intervals which are dependent on the comparison result from the comparison device.

12. The arrangement as claimed in one of the preceding 10 claims,

characterized

in that the data stream to be buffered comprises communications user data (KD) received via the communications network.

15

13. The arrangement as claimed in one of the preceding claims,

characterized

20 in that the time information transmitter (ZIG) has a detector device for ascertaining a temporarily low transmission volume in the communications network (KN), and

25 a transmission controller for triggering transmission of time information (ZI1, ZI2) when a low transmission volume is ascertained.

14. The arrangement as claimed in one of the preceding claims,

characterized

30 in that the communications system components (EAGA, EAGB) each have a PLL circuit for regulating the clock frequency of the clock generator (ZTG).

Abstract

Arrangement for synchronizing communications system components coupled via a communications network

To synchronize communications system components (EAGA, EAGB) coupled via a communications network (KN), a time information transmitter (ZIG) is provided which is used to transmit time information (ZI1, ZI2) - possibly on request - relating to the communications system components (EAGA, EAGB). The communications system components each have a clock generator (ZTG), which needs to be synchronized, and a real time clock (RTC), where the clock generator (ZTG) both determines the transmission rate for communications data (KD) which are to be transmitted and prescribes the time base for the real time clock (RTC). In addition, the communications system components (EAGA, EAGB) each have a comparison device (VE) for comparing received time information (ZI1, ZI2) with a current time value (ZR) indicated by the real time clock (RTC), and a clock frequency controller (TS1) for regulating the clock frequency of the clock generator (ZTG) on the basis of the comparison result.

FIGURE 2

FIG 1

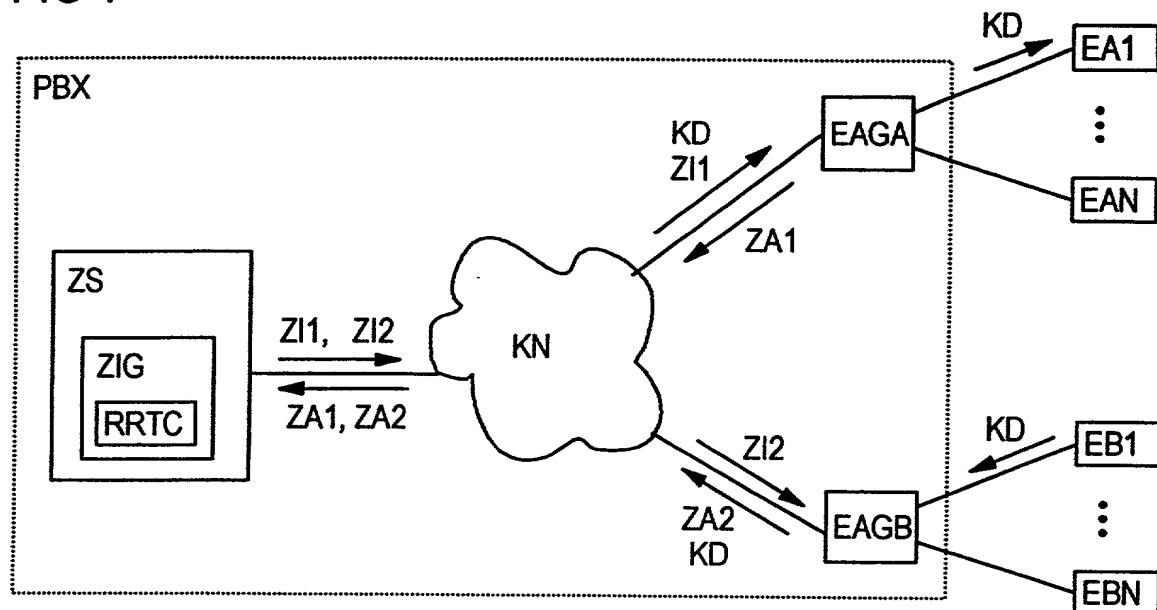
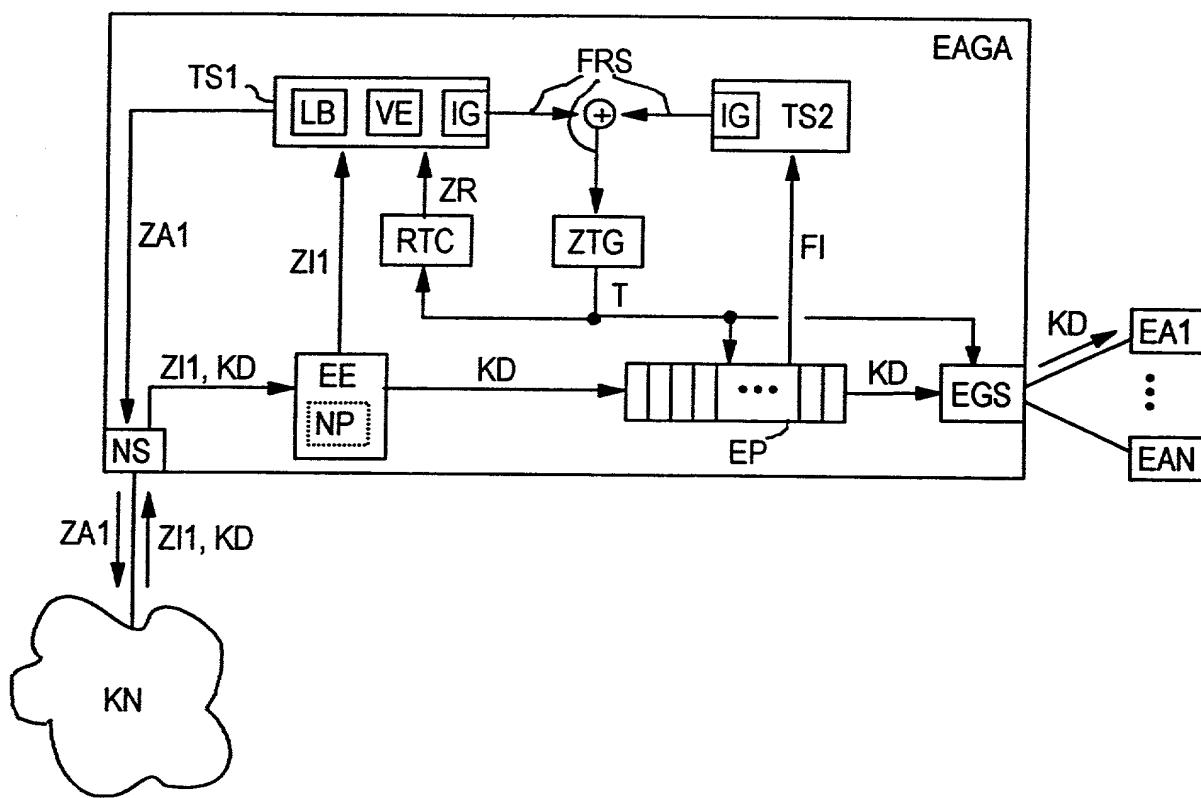


FIG 2



Declaration and Power of Attorney For Patent Application**Erklärung Für Patentanmeldungen Mit Vollmacht****German Language Declaration**

Als nachstehend benannter Erfinder erkläre ich hiermit an Eides Statt:

dass mein Wohnsitz, meine Postanschrift, und meine Staatsangehörigkeit den im Nachstehenden nach meinem Namen aufgeführten Angaben entsprechen,

dass ich, nach bestem Wissen der ursprüngliche, erste und alleinige Erfinder (falls nachstehend nur ein Name angegeben ist) oder ein ursprünglicher, erster und Miterfinder (falls nachstehend mehrere Namen aufgeführt sind) des Gegenstandes bin, für den dieser Antrag gestellt wird und für den ein Patent beantragt wird für die Erfindung mit dem Titel:

Anordnung zum Synchronisieren von
über ein Kommunikationsnetz
gekoppelten
Kommunikationssystemkomponenten

deren Beschreibung

(zutreffendes ankreuzen)

hier beigefügt ist.

am 07.09.2000 als

PCT internationale Anmeldung

PCT Anmeldungsnummer PCT/DE00/03105

eingereicht wurde und am _____

abgeändert wurde (falls tatsächlich abgeändert).

Ich bestätige hiermit, dass ich den Inhalt der obigen Patentanmeldung einschliesslich der Ansprüche durchgesehen und verstanden habe, die eventuell durch einen Zusatzantrag wie oben erwähnt abgeändert wurde.

Ich erkenne meine Pflicht zur Offenbarung irgendwelcher Informationen, die für die Prüfung der vorliegenden Anmeldung in Einklang mit Absatz 37, Bundesgesetzbuch, Paragraph 1.56(a) von Wichtigkeit sind, an.

Ich beanspruche hiermit ausländische Prioritätsvorteile gemäss Abschnitt 35 der Zivilprozeßordnung der Vereinigten Staaten, Paragraph 119 aller unten angegebenen Auslandsanmeldungen für ein Patent oder eine Erfindersurkunde, und habe auch alle Auslandsanmeldungen für ein Patent oder eine Erfindersurkunde nachstehend gekennzeichnet, die ein Anmeldedatum haben, das vor dem Anmeldedatum der Anmeldung liegt, für die Priorität beansprucht wird.

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

Arrangement for synchronizing
communication system components
coupled via a communication network

the specification of which

(check one)

is attached hereto.

was filed on 07.09.2000 as

PCT international application

PCT Application No. PCT/DE00/03105

and was amended on _____

(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

German Language Declaration

Prior foreign applications
Priorität beansprucht

Priority Claimed

| | | | | |
|----------------------|---------------------|--------------------------------------------------------|-----------------------------------------------|-------------------------------------|
| <u>19943779.3</u> | <u>DE</u> | <u>13.09.1999</u> | <input checked="" type="checkbox"/> Yes Ja | <input type="checkbox"/> No Nein |
| (Number) (Nummer) | (Country) (Land) | (Day Month Year Filed) (Tag Monat Jahr eingereicht) | | |
| | | | | |
| <u>(Number)</u> | <u>(Country)</u> | <u>(Day Month Year Filed)</u> | <input type="checkbox"/> Yes Ja | <input type="checkbox"/> No Nein |
| <u>(Nummer)</u> | <u>(Land)</u> | <u>(Tag Monat Jahr eingereicht)</u> | | |
| | | | | |
| <u>(Number)</u> | <u>(Country)</u> | <u>(Day Month Year Filed)</u> | <input type="checkbox"/> Yes Ja | <input type="checkbox"/> No Nein |
| <u>(Nummer)</u> | <u>(Land)</u> | <u>(Tag Monat Jahr eingereicht)</u> | | |

Ich beanspruche hiermit gemäss Absatz 35 der Zivilprozeßordnung der Vereinigten Staaten, Paragraph 120, den Vorzug aller unten aufgeführten Anmeldungen und falls der Gegenstand aus jedem Anspruch dieser Anmeldung nicht in einer früheren amerikanischen Patentanmeldung laut dem ersten Paragraphen des Absatzes 35 der Zivilprozeßordnung der Vereinigten Staaten, Paragraph 122 offenbart ist, erkenne ich gemäss Absatz 37, Bundesgesetzbuch, Paragraph 1.56(a) meine Pflicht zur Offenbarung von Informationen an, die zwischen dem Anmeldedatum der früheren Anmeldung und dem nationalen oder PCT internationalen Anmeldedatum dieser Anmeldung bekannt geworden sind.

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §122, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application.

| | | | |
|---------------------------------------------------|-------------------------------------------------|---------------------------------------------------|-----------------------------------------------|
| <u>PCT/DE00/03105</u> | <u>07.09.2000</u> | <u>anhängig</u> | <u>pending</u> |
| (Application Serial No.) (Anmeldeseriennummer) | (Filing Date D, M, Y) (Anmeldedatum T, M, J) | (Status) (patentiert, anhängig, aufgegeben) | (Status) (patented, pending, abandoned) |
| | | | |
| <u>(Application Serial No.)</u> | <u>(Filing Date D,M,Y)</u> | <u>(Status)</u> | <u>(Status)</u> |
| <u>(Anmeldeseriennummer)</u> | <u>(Anmeldedatum T, M; J)</u> | <u>(patentiert, anhängig, aufgegeben)</u> | <u>(patented, pending, abandoned)</u> |

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| Unterschrift des Erfinders | Datum | Inventor's signature |
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